1. (Twice Amended) A DC to DC power converter which delivers an output voltage and an output current at its output terminals, the converter comprising:

a controlled rectifier transistor through which current flows to the output terminals;

a control circuit which controls the output voltage; and

override control to the control circuit, responsive to a condition of the power converter or connected circuitry, to effect a minimum current limit of the output current at the output terminals.

2. (Amended) A power converter as claimed in claim 1 wherein the override control substantially eliminates negative current flow through the controlled rectifier.

12. (Amended) A power converter as claimed in claim 1 wherein the minimum current limit is a negative current.

56. (Twice Amended) A method of converting DC to DC power comprising:

providing a controlled rectifier transistor through which current flows to the output terminals;

controlling an output voltage through a control circuit; and

overriding control to the control circuit to effect a minimum current limit of output current at the output terminals.

Please add new Claims 78 through 84.

78. (New) A method as claimed in claim 56 wherein the minimum current limit is effected by controlling duty cycle of a regulating transistor to control voltage output that effects the minimum current limit.

79. (New) A method as claimed in claim 56 wherein the minimum current limit is effected without disabling the controlled rectifier transistor.

80. (New) A power converter as claimed in claim 1 wherein the override control circuitry controls duty cycle of a regulating transistor to control voltage output that effects the minimum current limit.